

### Faculty Profile

<b>Faculty Name</b>	Ms. Shivani
<b>Designation</b>	Assistant Professor
<b>Qualification</b>	Ph.D*, M.Tech, B.Tech
<b>Email</b>	Shivani.cse@piet.co.in
<b>Area of Interest</b>	Deep Learning
<b>Work Experience (Total)</b>	3.9 Years
<ul style="list-style-type: none"> <li>• Teaching</li> </ul>	3.9 years
<ul style="list-style-type: none"> <li>• Research</li> </ul>	3.9 Years
<ul style="list-style-type: none"> <li>• Industry</li> </ul>	NIL
<ul style="list-style-type: none"> <li>• Others</li> </ul>	NIL
<b>Courses taught at Diploma/ Post Diploma/ Under Graduate/ Post Graduate/ Post Graduate Diploma Level</b>	DAA,DSA,WT,SE,ST, NNFL, IF
<b>Membership of Professional Bodies</b>	NIL
<b>Research Publications</b>	
<ul style="list-style-type: none"> <li>• Research Papers UGC-CARE</li> </ul>	04
<ul style="list-style-type: none"> <li>• Research Papers SCOPUS</li> </ul>	01
<ul style="list-style-type: none"> <li>• Research Papers WoS/SCI/ABDC</li> </ul>	Communicated
<ul style="list-style-type: none"> <li>• List of Publications</li> </ul>	<ul style="list-style-type: none"> <li>• A Genetic Improved Quantum Cryptography Model to Optimize Network Communication, published in International Journal of Innovative Technology and Exploring Engineering (IJITEE), Vol. 8, Issue-9S, July 2019, ISSN: 2278-3075,pp. 256-259.</li> <li>• Accuracy Improvement in Intruder Detection using Optimally Selected Features, published in <i>International Journal of Research in Electronics and Computer Engineering (IJRECE)</i>,Vol. 6, Issue 3, July-Sep 2018, ISSN: 2348-2281, pp.215-221.</li> <li>• A Comparative Study: Reviewing</li> </ul>

	<p>Performance of Routing Protocols in MANETs”, published in International Journal of Scientific Research in Science, Engineering and Technology (<i>IJSRSET</i>), Vol. 5, Issue 8, May-June 2018, ISSN: 2394-4099, pp. 528-532.</p> <ul style="list-style-type: none"> <li>• Securing Communication in MANET through E-GAMAN Algorithm, published in International Journal of Creative Research Thoughts (<i>IJCRT</i>), Vol. 6, Issue 2, April 2018,ISSN: 2320-2882,pp.403-407.</li> <li>• A Modified Approach to Improve Security and Congestion Control in AODV Routing Protocol against Blackhole Attack, published in International Journal on Recent and Innovation Trends in Computing and Communication (<i>IJRITCC</i>),Vol. 5, Issue 6, June 2017,ISSN: 2321-8169, pp. 266-272.</li> </ul>
<b>Book and Chapter Publications</b>	
• Books Authored published by International Publishers	01
• Books Authored published by National Publishers	NIL
• Publication of Chapter in Edited Books	03
• Editor of Book by International Publishers	NIL
• Editor of Book by National Publishers	NIL
• Translation Work of Book	NIL
• List of Book and Chapter Publications	<ul style="list-style-type: none"> <li>• A Comparative investigation of Consensus Algorithms in collaboration with IoT and Blockchain in Springer Book series Transforming Cybersecurity Solutions using</li> </ul>

	<p>Blockchain, Series Title: Blockchain Technologies, Series ISSN:2661-8338, eBook ISBN:978-981-336-858-3, DOI:10.1007/978-981-33-6858-3.</p> <ul style="list-style-type: none"> <li>• Role of Machine learning for Adhoc Networks, published in Cloud and IoT Based Vehicular Ad-Hoc Networks(cibvan-2021) book series chapter by, John Wiley &amp; Sons, page no 269, ISSN No 978-1119761839.</li> <li>• Bio-Inspired routing in VANET”published in Cloud and IoT Based Vehicular Ad-Hoc Networks(cibvan-2021) book chapter series by John Wiley &amp; Sons, page no 199, ISSN no: 978-1119761839.</li> <li>• Big Data A Practical Approach, published in International Institute of Organized Research (I2OR), INDIA , 978-81-942293-3-9, April 2020.</li> </ul>
<b>Patents</b>	
<ul style="list-style-type: none"> <li>• Published</li> </ul>	01
<ul style="list-style-type: none"> <li>• List of published patent(s)</li> </ul>	Patent published on “Low-K Side-Wall Spacer Based Inner Gate”of application no 202011033495, date of filing :5-8-20.
<ul style="list-style-type: none"> <li>• Filed</li> </ul>	NIL
<ul style="list-style-type: none"> <li>• List of filed patent(s)</li> </ul>	NIL
<b>PhD Guidance</b>	
<ul style="list-style-type: none"> <li>• Degree Awarded</li> </ul>	NIL
<ul style="list-style-type: none"> <li>• Thesis Submitted</li> </ul>	NIL
<b>M.Tech. Guidance</b>	
<ul style="list-style-type: none"> <li>• Degree Awarded</li> </ul>	NIL
<ul style="list-style-type: none"> <li>• Thesis Submitted</li> </ul>	NIL
<b>Research Project</b>	

<ul style="list-style-type: none"> <li>List of Research project</li> </ul>	NIL
<b>Consultancy</b>	
<ul style="list-style-type: none"> <li>List of Consultancy</li> </ul>	NIL
<b>Awards &amp; Honours</b>	
<ul style="list-style-type: none"> <li>List of Awards &amp; Honours</li> </ul>	NIL
<b>Invited lectures / Resource Person/ paper presentation in Seminars/ Conferences/full paper in Conference</b>	
<ul style="list-style-type: none"> <li>International (Abroad)</li> </ul>	
<ul style="list-style-type: none"> <li>International (Within Country)</li> </ul>	01
<ul style="list-style-type: none"> <li>National</li> </ul>	01
<ul style="list-style-type: none"> <li>List of published papers</li> </ul>	<ul style="list-style-type: none"> <li>A Brief Enlightenment of Applications for Deep Learning” published in 4th International Conference on Recent Trends in Communication&amp; Electronics (ICCE-2020) approved by CRC Press (Taylor &amp; Francis Group) and Sponsored by Govt. of India, AICTE New Delhi organized by Department of Electronics &amp; Communication Engineering, KIET Group of Institutions, Delhi-NCR, Meerut Road (NH-58), Ghaziabad.</li> <li>A Review of Security Analyses in AODV Routing Protocol, Second National Conference on Recent Trends in Computing and Communication Technologies (RCCT), Murthal, Sonipat, Mar 2, 2017, Proceedings ISBN Number: 978-93-86256-54-6, pp. 199-206.</li> </ul>
<b>Organizing National Conference/ International Conference/ FDP/STTP</b>	
<ul style="list-style-type: none"> <li>List of Conference/FDP/STTP committee</li> </ul>	<ul style="list-style-type: none"> <li>Member – Organizing Committee of FDP on “Soft Computing Applications to Design of</li> </ul>

	<p>Intelligent Systems” at Panipat Institute of Engineering and Technology, Panipat, Haryana</p> <ul style="list-style-type: none"><li>• FDP: soft Computing(Organising Committee Member)</li><li>• Dell EMC FDP :Technical Committee Member</li></ul>
<b>Social Contributions and Sports</b>	
<ul style="list-style-type: none"><li>• <b>List of Social Contributions and Sports</b></li></ul>	